

FIG. 1

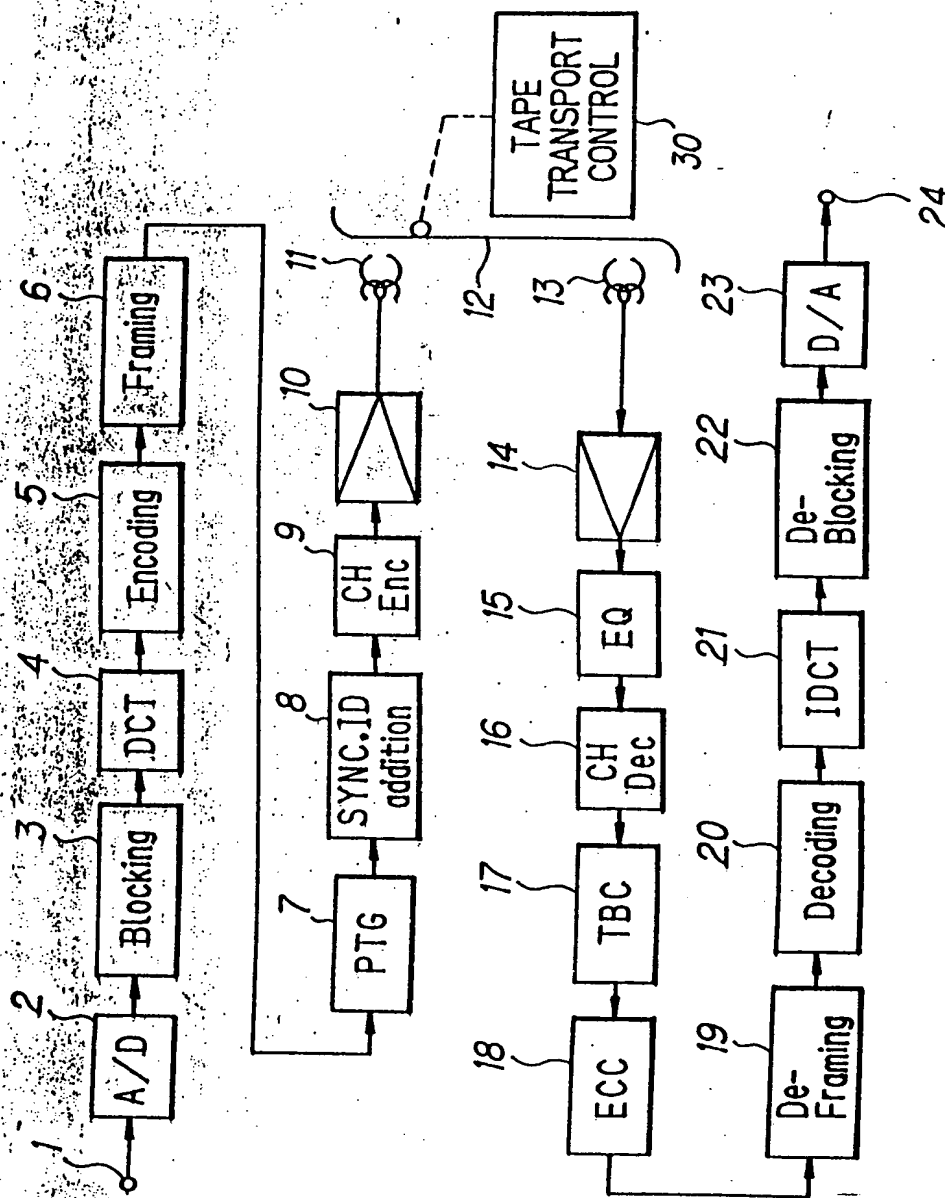


FIG. 2

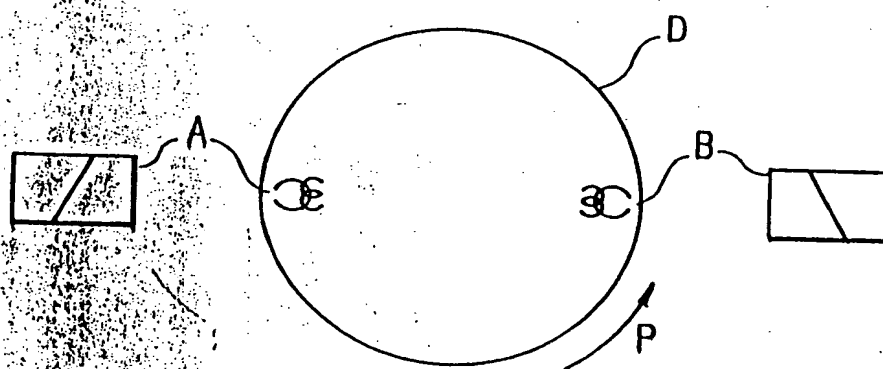


FIG. 3

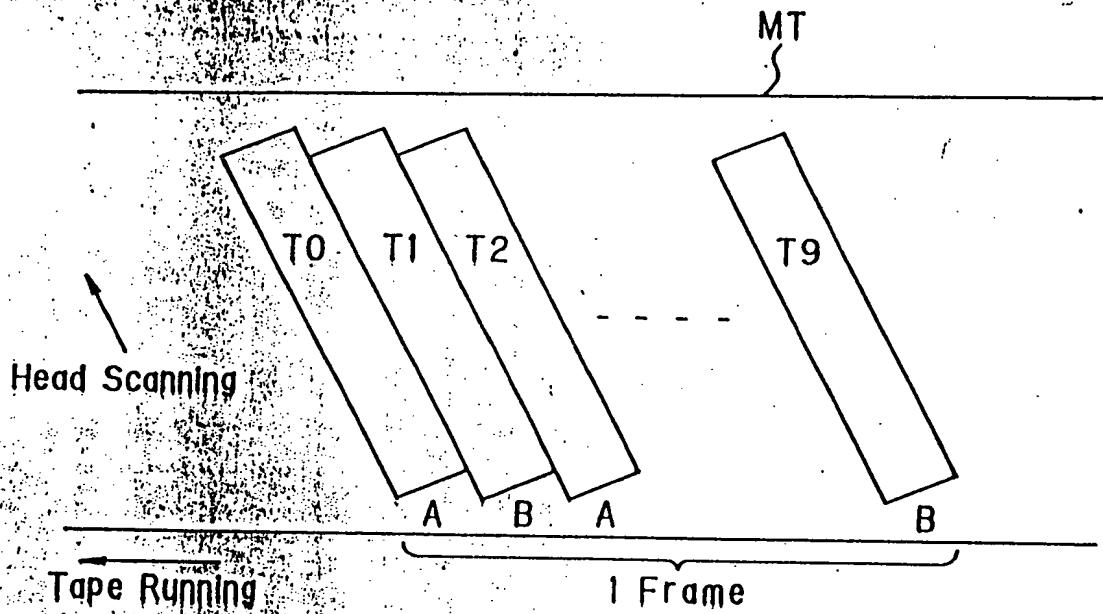


FIG. 4

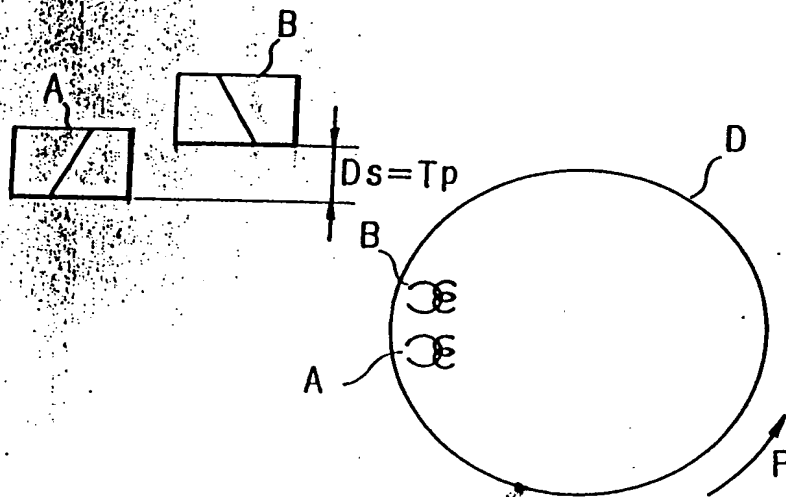


FIG. 5

(a) Y

720 Sample				480 Sample
1-1	1-2	.	1-90	
2-1	2-2	.	2-90	
.	.	.	.	
60-1	60-2	.	60-90	480 Sample
.	.	.	.	

FIG. 5

(b) R-Y
or
B-Y

180 Sample			480 Sample
1-1	.	1-23	
2-1	.	2-23	
.	.	.	
60-1	.	60-23	480 Sample
.	.	.	

FIG. 5

(c) MB

$$\begin{array}{|c|c|c|c|} \hline Y & Y & Y & Y \\ \hline \end{array} + \begin{array}{|c|} \hline R-Y \\ \hline \end{array} + \begin{array}{|c|} \hline B-Y \\ \hline \end{array}$$

FIG. 6

(a)

14	14	9	14	14	9
Y	Y	B-Y	Y	Y	R-Y
Y	Y	B-Y	Y	Y	R-Y
Y	Y	B-Y	Y	Y	R-Y
Y	Y	B-Y	Y	Y	R-Y
Y	Y	B-Y	Y	Y	R-Y

FIG. 6

(b)

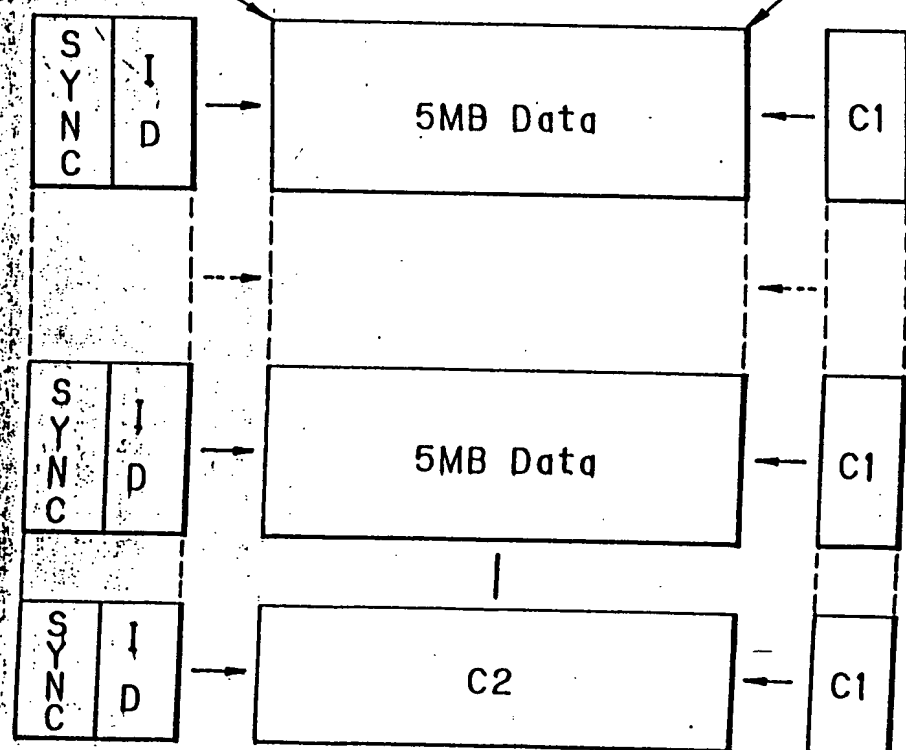


FIG. 7

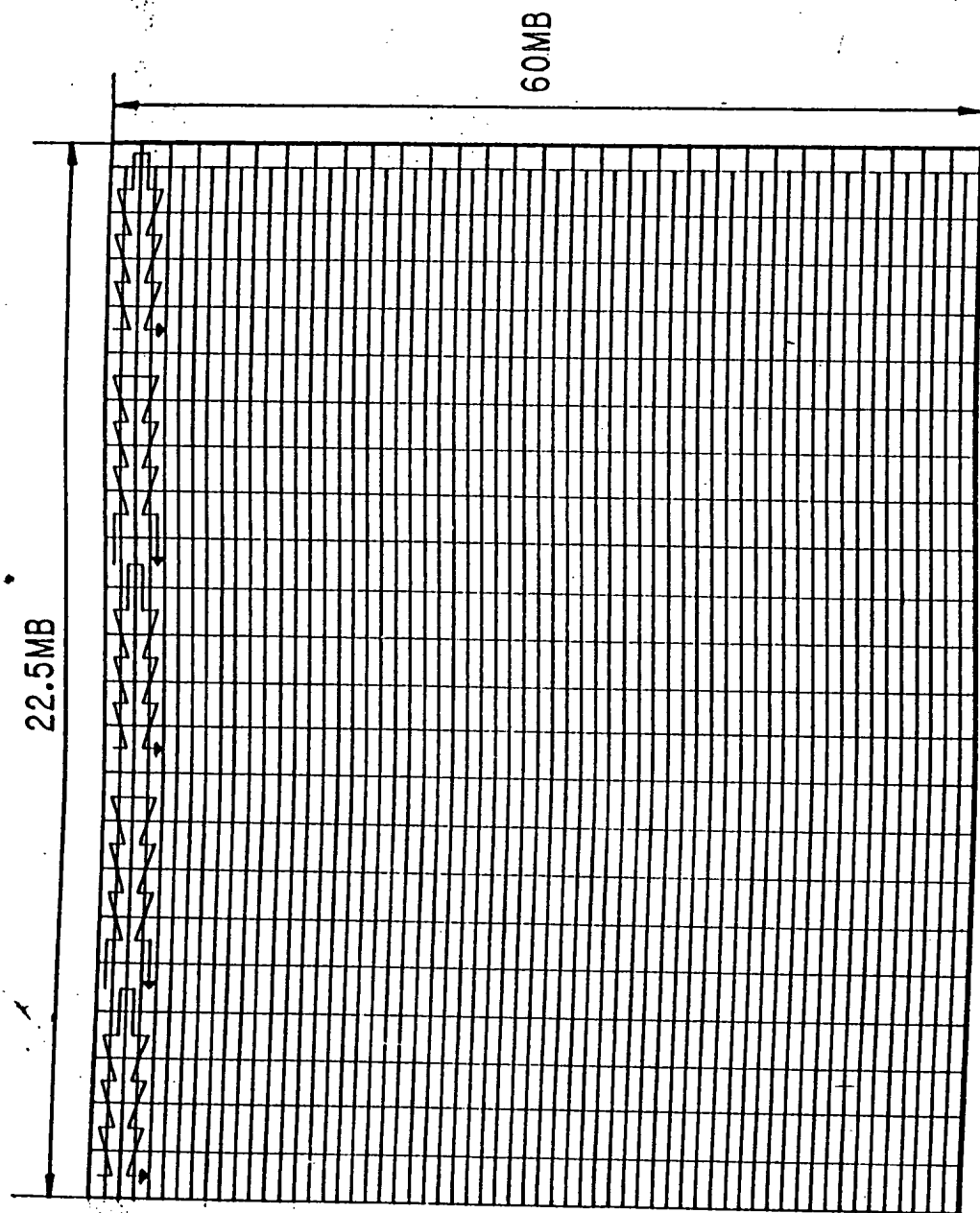


FIG. 8

(a)

A0	A1	A2	A3		
B0	B1	B2	B3	A4	
A8	A7	A6	A5	B4	
B8	B7	B6	B5		
A9	A10	A11	A12		
B9	B10	B11	B12	A13	
A17	A16	A15	A14	B13	
B17	B16	B15	B14		
A18	A19	A20	A21		
B18	B19	B20	B21	A22	
A26	A25	A24	A23	B22	
B26	B25	B24	B23		

FIG. 8

(b)

A0	B0	A1	B1	A2	B2	A3	B3	A4
B8	A8	B7	A7	B6	A6	B5	A5	B4
A9	B9	A10	B10	A11	B11	A12	B12	A13
B17	A17	B16	A16	B15	A15	B14	A14	B13
A18	B18	A19	B19	A20	B20	A21	B21	A22
B26	A26	B25	A25	B24	A24	B23	A23	B22

FIG. 8

(c)

45MB					30MB
0	4	8	2	6	
2	6	0	4	8	
4	8	2	6	0	
6	0	4	8	2	
8	2	6	0	4	

FIG. 9

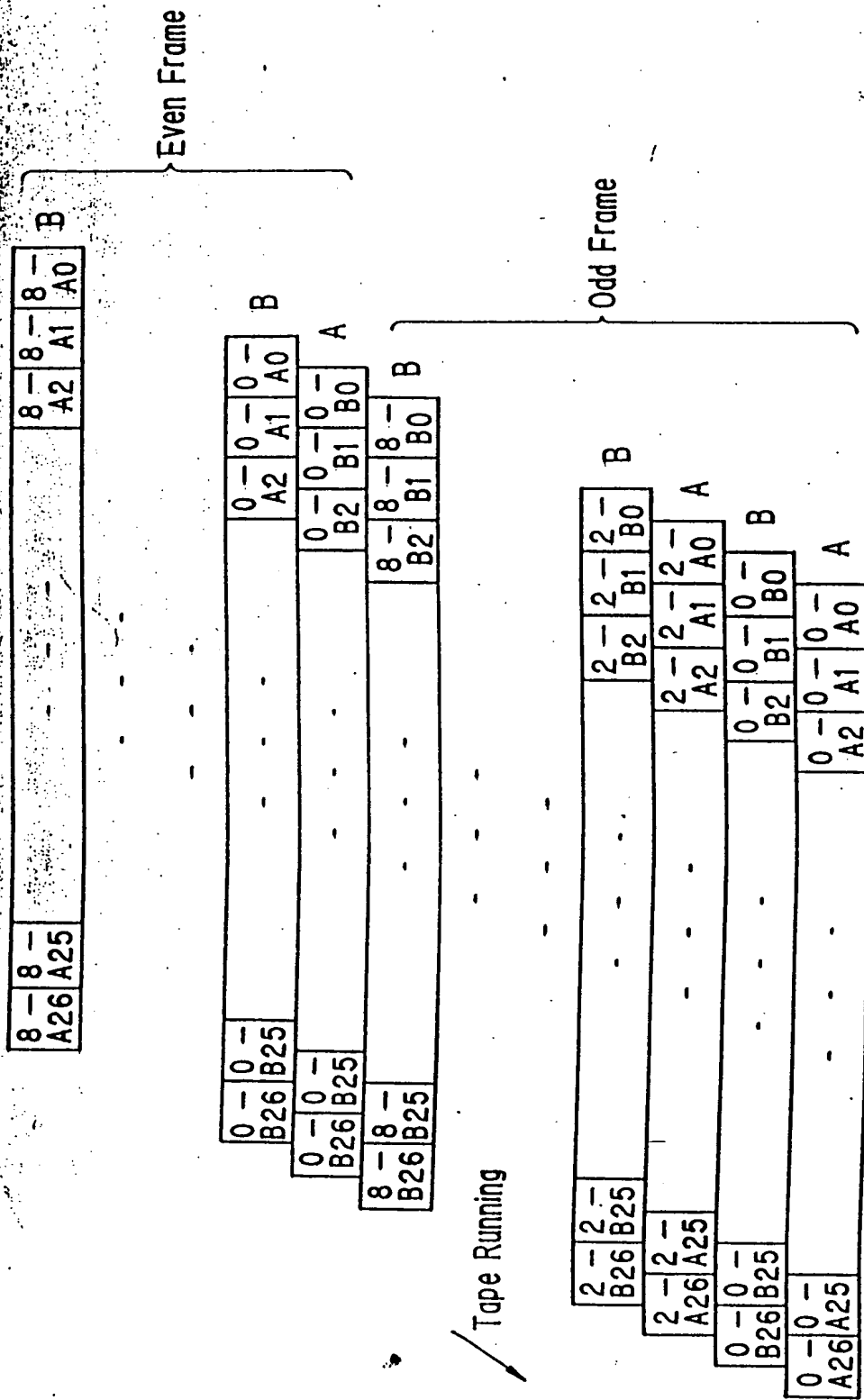
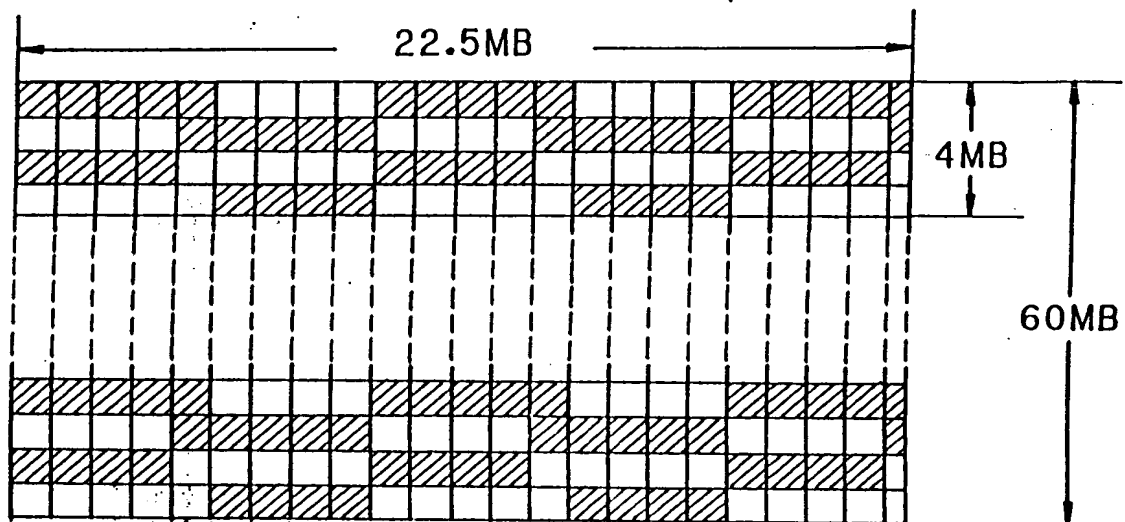


FIG. 10



Data recorded on odd-number tracks of Odd Frames
(even-number tracks of even frames)



Data recorded on even-number tracks of Even Frames
(odd-number tracks of odd frames)

FIG. 11

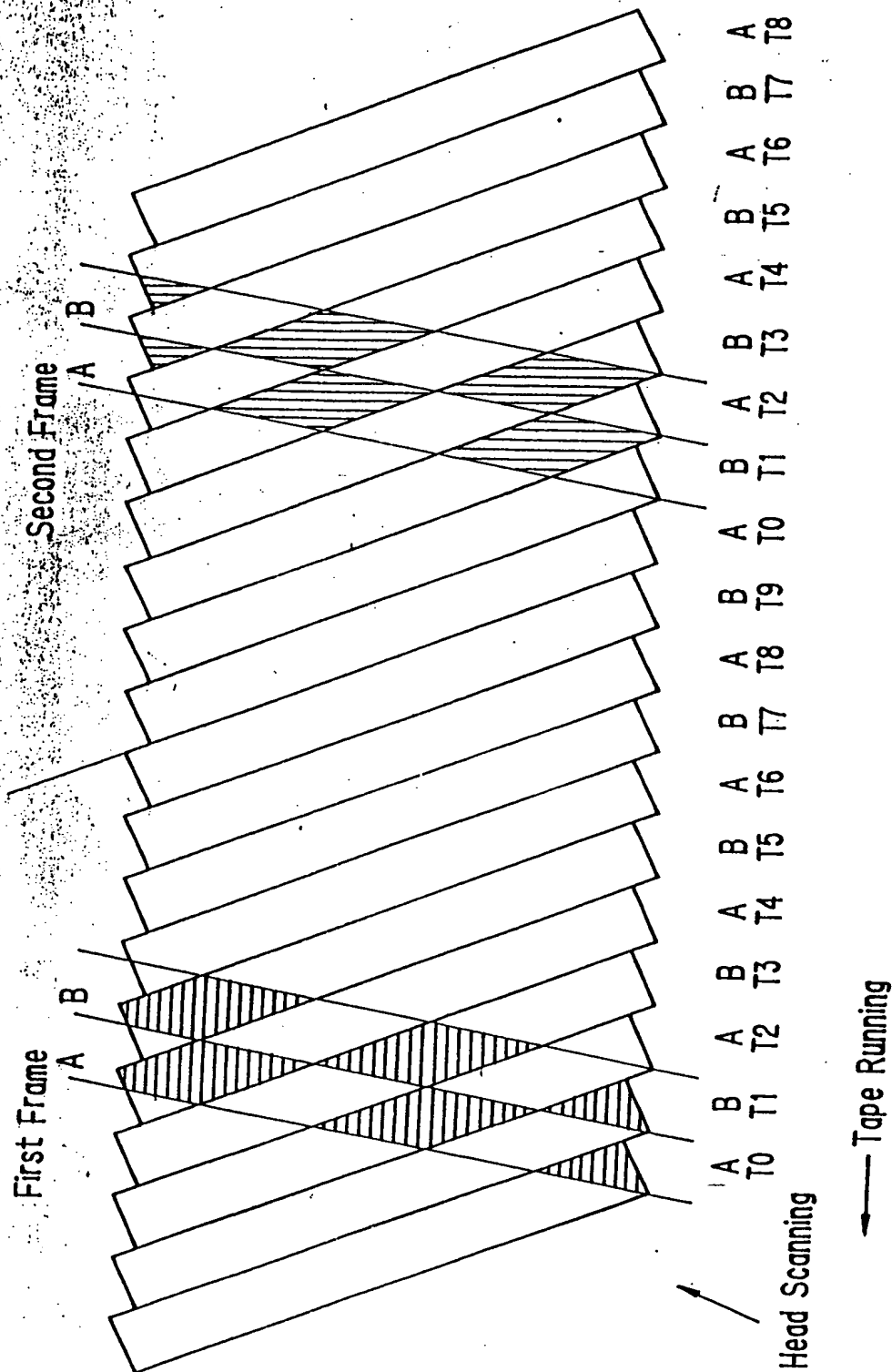
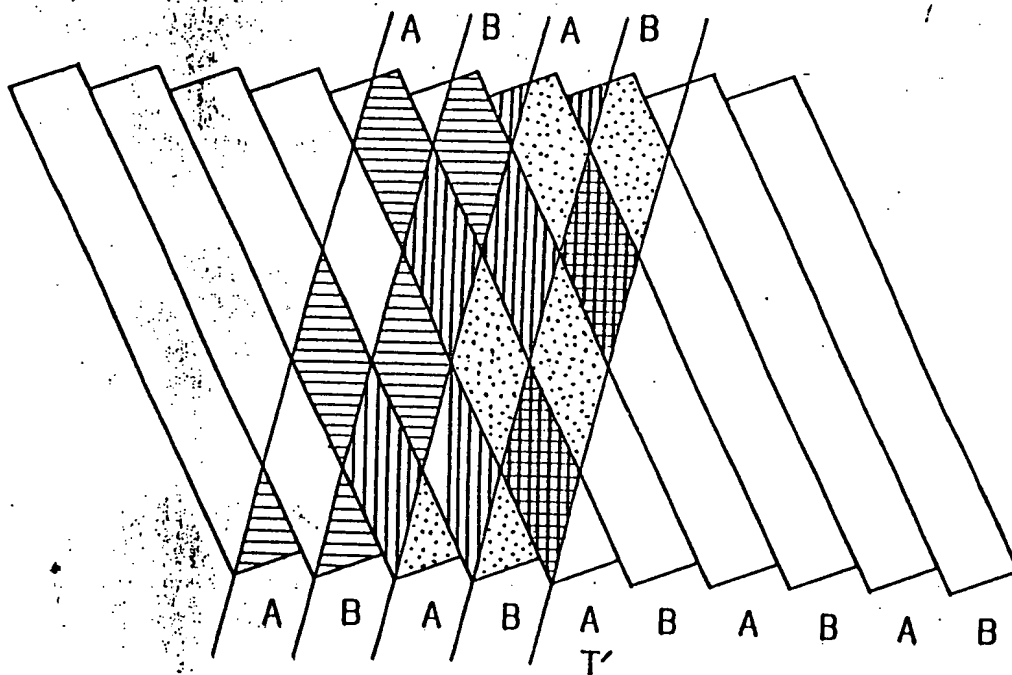


FIG. 12



Data obtained from First Frame
 Data obtained from Second Frame
 Data obtained from Third Frame
 Data obtained from Fourth Frame

FIG. 13

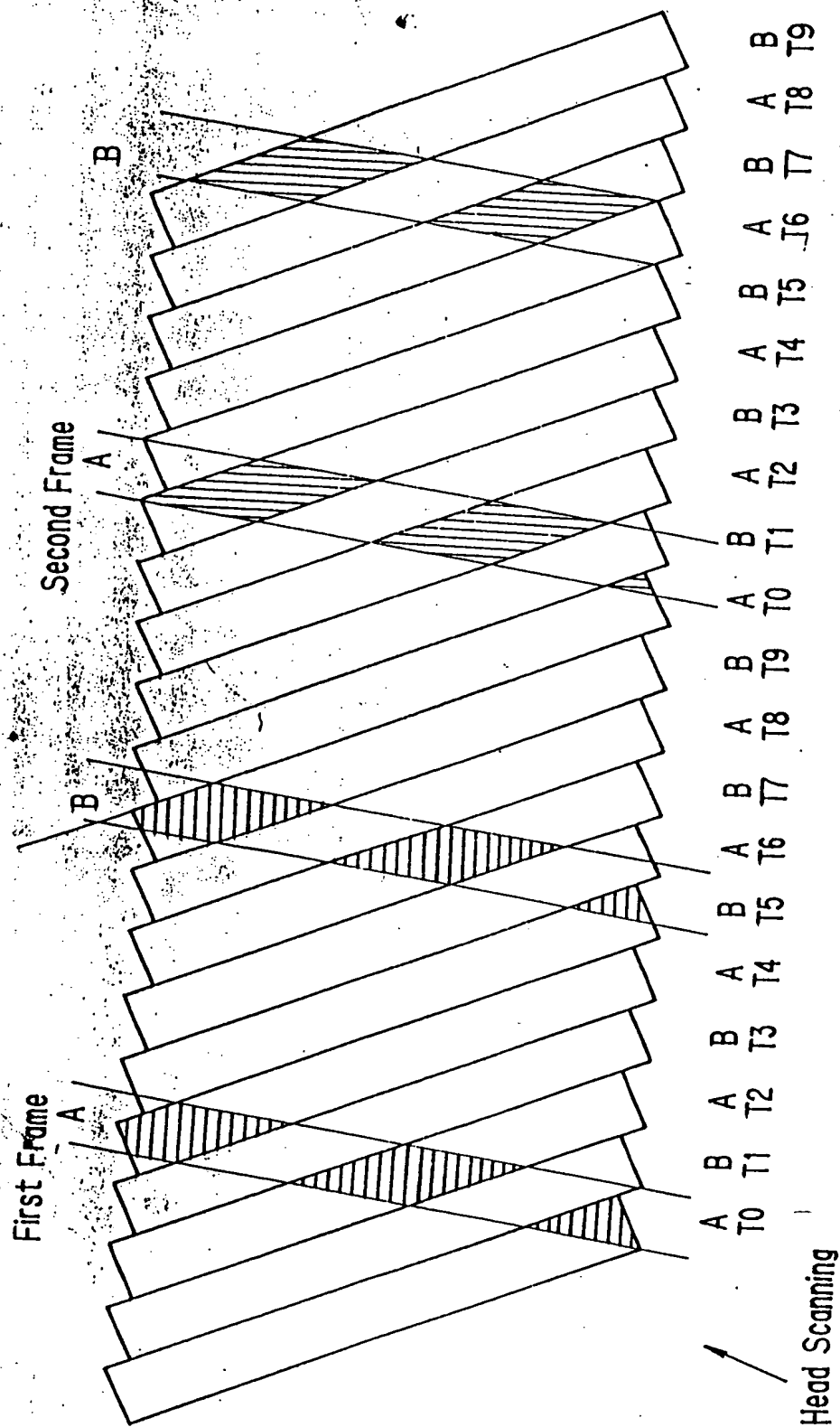
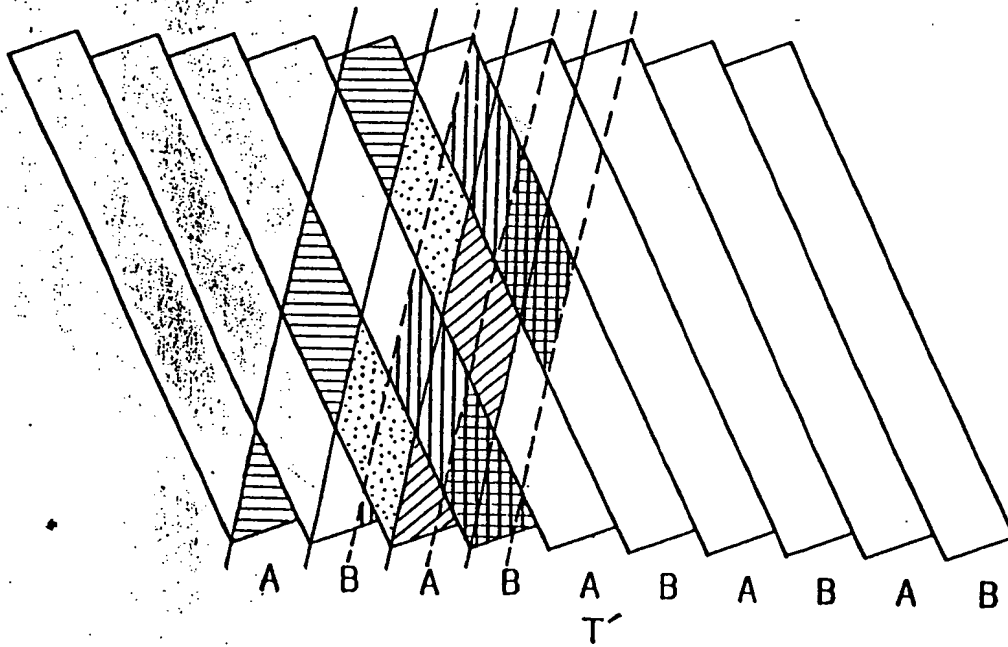


FIG. 14








-  Data obtained from First Frame
-  Data obtained from Second Frame
-  Data obtained from Third Frame
-  Data obtained from Fourth Frame
-  Data obtained from Fifth Frame

FIG. 15
(a) Y

720 Sample				576 Sample
1-1	1-2	.	.	
2-1	2-2	.	.	
72-1	72-2	.	.	

FIG. 15
(b) R-Y
or
B-Y

360 Sample			288 Sample
1-1	.	1-45	
2-1	.	2-45	
36-1	.	36-45	

FIG. 15
(c) MB

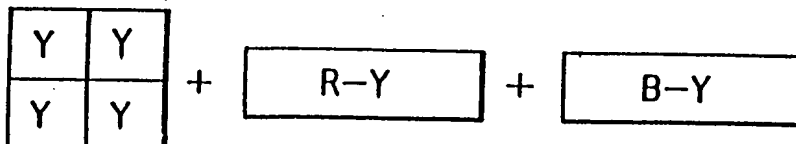


FIG. 16
(a)

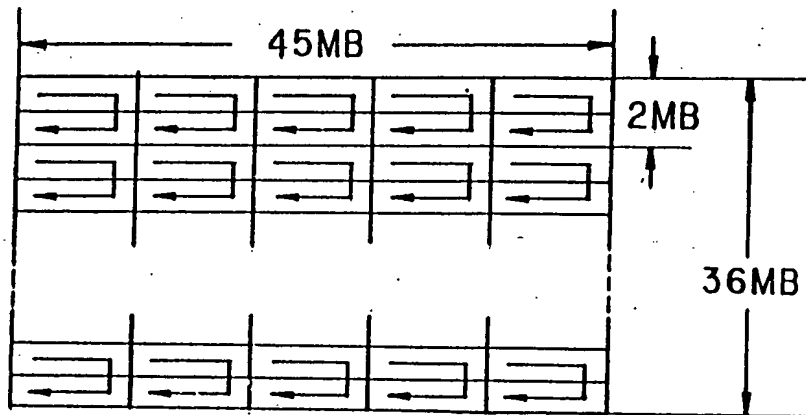


FIG. 16
(b)

A0	B0	A1	B1	A2	B2	A3	B3	A4
B8	A8	B7	A7	B6	A6	B5	A5	B4
A9	B9	A10	B10	A11	B11	A12	B12	A13
B17	A17	B16	A16	B15	A15	B14	A14	B13
A18	B18	A19	B19	A20	B20	A21	B21	A22
B26	A26	B25	A25	B24	A24	B23	A23	B22

FIG. 16
(c)

Diagram illustrating a memory layout. The total width is 45MB and the total height is 36MB. The layout is divided into a grid of 5 columns and 6 rows. Each cell in the grid contains a numerical value. The values are arranged in a pattern that suggests a specific data structure or mapping.

0	6	10	2	8
2	8	0	4	10
4	10	2	6	0
6	0	4	8	2
8	2	6	10	4
10	4	8	0	6

10- A26	10- A25		10- A2	10- A1	10- A0	B
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0 - 0 -	0 - 0 -	0 - 0 -	0 - 0 -	0 - 0 -
B26 B25		A2	A1	A0
0 - 0 -		0 - 0 -	0 - 0 -	0 - 0 -
B26 B25		B2	B1	B0
10 - 10 -		10 - 10 -	10 - 10 -	10 - 10 -
B26 B25		B2	B1	B0

[illegible]

Head Scanning

FIG. 18

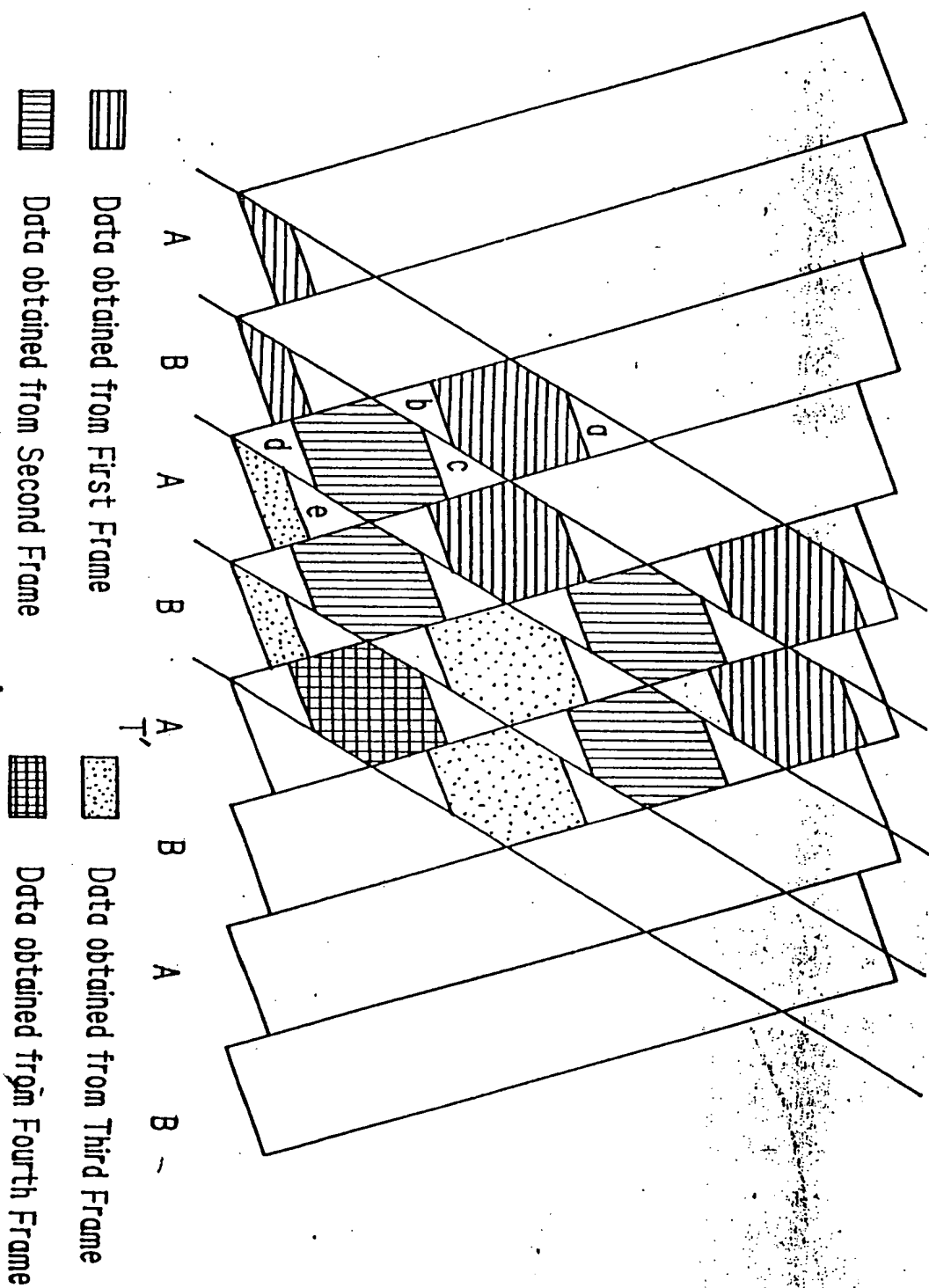


FIG. 19

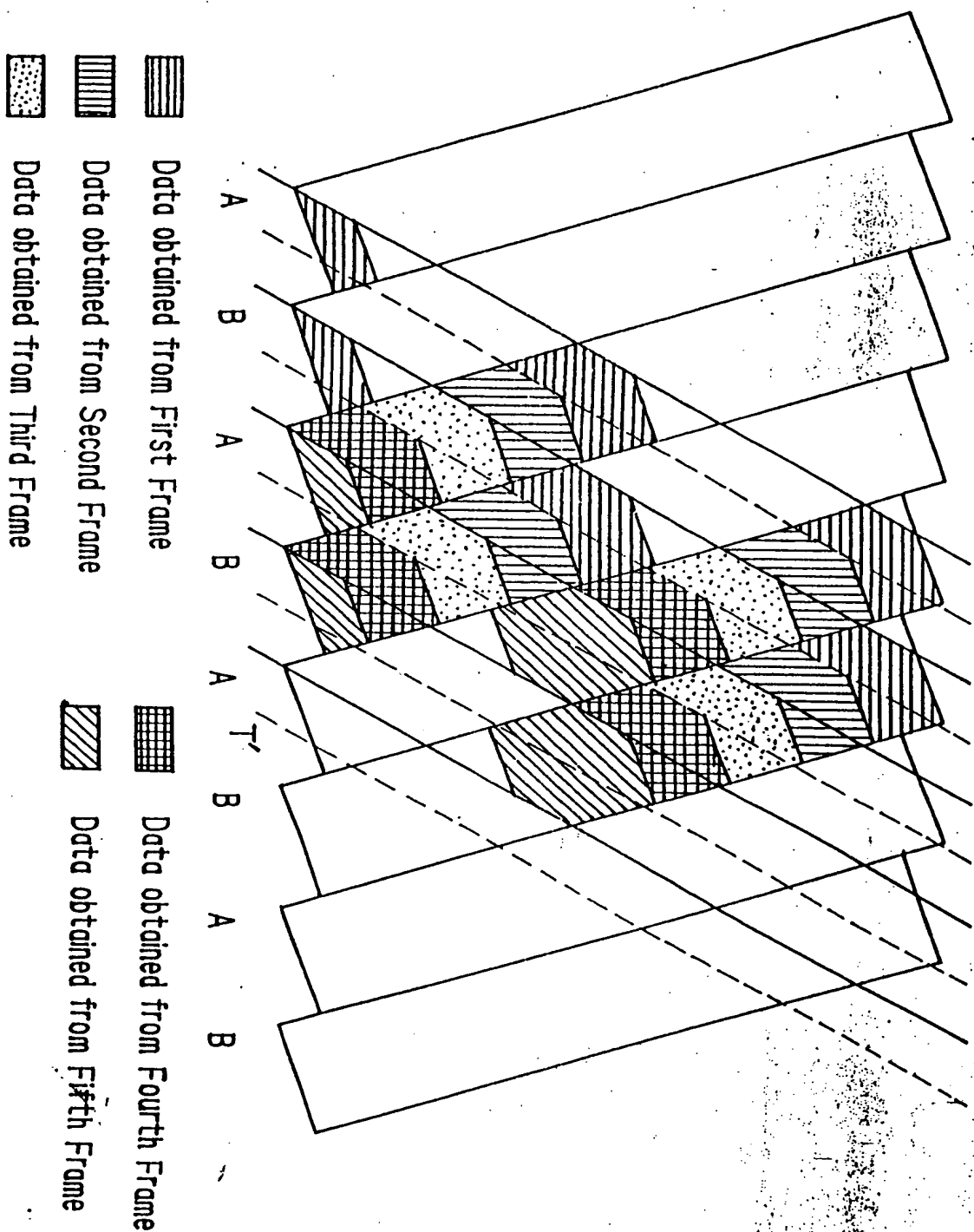


FIG. 20

